

## Inventor Name Search Result

Your Search was:

Last Name = LOVE

First Name = SIMON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07084952</a>	<a href="#">4870561</a>	150	07/20/1987	USER INTERFACE SIMULATION AND MANAGEMENT FOR PROGRAM-CONTROLLED APPARATUS	LOVE, SIMON
<a href="#">09549336</a>	Not Issued	071	04/14/2000	SERVICE DETAIL RECORDS FOR LAYERED COMMUNICATIONS PROTOCOLS	LOVE, SIMON
<a href="#">09703542</a>	Not Issued	094	11/01/2000	SYSTEM AND METHOD FOR MONITORING COMMUNICATION NETWORKS USING DATA STREAM CHARACTERIZATION	LOVE, SIMON
<a href="#">09704002</a>	<a href="#">6738355</a>	150	11/01/2000	SYNCHRONIZATION METHOD FOR MULTI-PROBE COMMUNICATIONS NETWORK MONITORING	LOVE, SIMON
<a href="#">09704051</a>	<a href="#">6831890</a>	150	10/31/2000	MEASURING NETWORK PERFORMANCE PARAMETERS IN DATA COMMUNICATION NETWORKS	LOVE, SIMON
<a href="#">09735607</a>	Not Issued	071	12/14/2000	MEASURING EFFICIENCY OF DATA TRANSMISSION	LOVE, SIMON
<a href="#">10726439</a>	Not Issued	030	12/03/2003	COMPENSATED REFRESH OSCILLATOR	LOVETT, SIMON
<a href="#">08958464</a>	<a href="#">6097222</a>	150	10/27/1997	SYMMETRICAL NOR GATES	LOVETT, SIMON J
<a href="#">09521190</a>	<a href="#">6292403</a>	150	03/07/2000	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
<a href="#">09735526</a>	<a href="#">6664799</a>	150	12/14/2000	METHOD AND APPARATUS FOR ENABLING A DIGITAL MEMORY TESTER TO READ THE FREQUENCY OF A FREE RUNNING OSCILLATOR	LOVETT, SIMON J.
<a href="#">09878433</a>	<a href="#">6385128</a>	150	06/11/2001	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
<a href="#">09878434</a>	<a href="#">6445645</a>	150	06/11/2001	RANDOM ACCESS MEMORY HAVING INDEPENDENT READ PORT AND	LOVETT, SIMON J.

				WRITE PORT AND PROCESS FOR WRITING TO AND READING FROM THE SAME	
<u>09915823</u>	<u>6538466</u>	150	07/26/2001	BUFFER WITH STABLE TRIP POINT	LOVETT, SIMON J.
<u>09942898</u>	Not Issued	092	08/30/2001	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
<u>10102221</u>	<u>6690606</u>	150	03/19/2002	ASYNCHRONOUS INTERFACE CIRCUIT AND METHOD FOR A PSEUDO-STATIC MEMORY DEVICE	LOVETT, SIMON J.
<u>10225423</u>	<u>6750497</u>	150	08/22/2002	HIGH-SPEED TRANSPARENT REFRESH DRAM-BASED MEMORY CELL	LOVETT, SIMON J.
<u>10228932</u>	<u>6879538</u>	150	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
<u>10230046</u>	<u>6845054</u>	150	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
<u>10232935</u>	Not Issued	094	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
<u>10357862</u>	Not Issued	092	02/03/2003	DETECTION CIRCUIT FOR MIXED ASYNCHRONOUS AND SYNCHRONOUS MEMORY OPERATION	LOVETT, SIMON J.
<u>10436166</u>	<u>6839297</u>	150	05/13/2003	HIGH-SPEED TRANSPARENT REFRESH DRAM-BASED MEMORY CELL	LOVETT, SIMON J.
<u>10613236</u>	Not Issued	030	07/03/2003	TEMPERATURE SENSING DEVICE IN AN INTEGRATED CIRCUIT	LOVETT, SIMON J.
<u>10754658</u>	Not Issued	093	01/08/2004	ASYNCHRONOUS INTERFACE CIRCUIT AND METHOD FOR A PSEUDO-STATIC MEMORY DEVICE	LOVETT, SIMON J.
<u>10873968</u>	Not Issued	030	06/22/2004	APPARATUS AND METHOD FOR IMPROVING DYNAMIC REFRESH IN A MEMORY DEVICE	LOVETT, SIMON J.
<u>10969184</u>	Not Issued	030	10/21/2004	METHOD OF OPERATING A MEMORY CELL	LOVETT, SIMON J.
<u>08958500</u>	<u>5889416</u>	150	10/27/1997	SYMMETRICAL NAND GATES	LOVETT, SIMON J.
<u>09021461</u>	<u>6278295</u>	150	02/10/1998	BUFFER WITH STABLE TRIP POINT	LOVETT, SIMON J.
<u>09238270</u>	<u>6069839</u>	150	01/27/1999	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
<u>09238953</u>	<u>6262936</u>	150	01/27/1999	RANDOM ACCESS MEMORY HAVING INDEPENDENT READ PORT AND WRITE PORT AND PROCESS FOR WRITING TO AND READING FROM	LOVETT, SIMON J.

				THE SAME	
<u>09238954</u>	<u>6262937</u>	150	01/27/1999	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
<u>09277616</u>	<u>6100560</u>	150	03/26/1999	NONVOLATILE CELL	LOVETT, SIMON J.
<u>09458776</u>	<u>6181621</u>	150	12/10/1999	THRESHOLD VOLTAGE MISMATCH COMPENSATED SENSE AMPLIFIER FOR SRAM MEMORY ARRAYS	LOVETT, SIMON J.
<u>60077982</u>	Not Issued	159	03/13/1998	RANDOM ACCESS MEMORY AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
<u>60078029</u>	Not Issued	159	03/13/1998	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
<u>60078718</u>	Not Issued	159	03/20/1998	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
<u>08774293</u>	<u>5936973</u>	150	12/23/1996	TEST MODE LATCHING SCHEME	LOVETT, SIMON JOHN

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

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## Inventor Name Search Result

Your Search was:

Last Name = ARCHIBALD

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09735607</a>	Not Issued	071	12/14/2000	MEASURING EFFICIENCY OF DATA TRANSMISSION	ARCHIBALD, DAVID
<a href="#">09679078</a>	Not Issued	041	10/05/2000	CORRELATION OF SIGNALLING MESSAGES	ARCHIBALD, DAVID MOIR
<a href="#">09834046</a>	Not Issued	164	04/12/2001	GENERATING CALL DETAIL RECORDS	ARCHIBALD, DAVID MOIR
<a href="#">10151674</a>	Not Issued	030	05/20/2002	SYSTEM AND METHOD FOR CORRELATION AND REAL-TIME DISPLAY OF MAPPED PSTN TRUNK IDENTIFIERS AND GATEWAY CONTROL PROTOCOL ENDPOINTS	ARCHIBALD, DAVID. MOIR
<a href="#">10222994</a>	Not Issued	030	08/19/2002	GENERATING CALL DETAIL RECORDS	ARCHIBALD, DAVID MOIR
<a href="#">09367396</a>	Not Issued	041	08/13/1999	MONITORING ISDN LINKS	ARCHIBALD, DAVID MOIR
<a href="#">09451893</a>	Not Issued	061	12/01/1999	MONITORING LINKS IN ATM NETWORKS	ARCHIBALD, DAVID MOIR
<a href="#">06843290</a>	<a href="#">4662642</a>	250	03/24/1986	ROPING DUMMY FOR PRACTICING ROPING OF CALVES	ARCHIBALD, DAVID R.
<a href="#">06764054</a>	<a href="#">4645191</a>	150	08/09/1985	VARIABLE SPEED CONVEYOR APPARATUS	ARCHIBALD, DAVID W

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